

Substitute for form 1449/PTO				Complete if Known	
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	09/929,178
				Filing Date	August 14, 2001
				First Named Inventor	Donald P. MATTHEWS, Jr.
				Art Unit	2137
				Examiner Name	Popham, Jeffrey D.
Sheet	1	of	2	Attorney Docket Number	2875.0500001

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	US1	3,962,539	06-08-1976	Ehram et al.	
	US2	4,914,694	04-03-1990	Leonard et al.	
	US3	5,058,048	10-15-1991	Gupta et al.	
	US4	5,144,574	09-01-1992	Morita	
	US5	5,267,186	11-30-1993	Gupta et al.	
	US6	5,305,453	04-19-1994	Boudry et al.	
	US7	5,315,178	05-24-1994	Snider	
	US8	5,317,638	05-31-1994	Kao et al.	
	US9	5,347,580	09-13-1994	Molva et al.	
	US10	5,384,723	01-24-1995	Karim et al.	
	US11	5,426,600	06-20-1995	Nakagawa et al.	
	US12	5,428,745	06-27-1995	de Bruijn et al.	
	US13	5,459,681	10-17-1995	Harrison et al.	
	US14	5,519,603	05-21-1996	Allbery et al.	
	US15	5,561,770	10-01-1996	de Bruijn et al.	
	US16	5,740,249	04-14-1998	Shimizu et al.	
	US17	5,796,837	08-18-1998	Kim et al.	
	US18	5,918,075	06-29-1999	Paysan	
	US19	5,923,574	07-13-1999	Bechade	
	US20	6,028,939	02-22-2000	Yin	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1	EP 0 802 653	10-22-1997	Buer		
	FP2	EP 1 191 736	03-27-2002	Matthews, Jr.		
	FP3	EP 1 191 738	03-27-2002	Tham et al.		
	FP4	WO 99/14881	03-25-1999	Kaplan et al.		
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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	US21	6,088,800	07-11-2000	Jones et al.	
	US22	6,118,300	09-12-2000	Wittig et al.	
	US23	6,182,104	01-30-2001	Foster et al.	
	US24	6,199,162	03-06-2001	Lyuster	
	US25	6,295,606	09-25-2001	Messerges et al.	
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	US28	6,882,727	04-19-2005	Vialen et al.	
	US29	6,940,975	09-06-2005	Kawamura et al.	
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	US33	2002-0106078	08-08-2002	Qi et al.	
	US34	2002-0106080	08-08-2002	Qi et al.	
	US35	2002-0108048	08-08-2002	Qi et al.	
	US36	2002-0061107	05-23-2002	Tham et al.	
	US37	2005-0063538	03-24-2005	Tham et al.	
	US38				
	US39				
	US40				

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	FP11					
	FP12					
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NON PATENT LITERATURE DOCUMENTS			
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	NPL1	Zheng Qi and Mark Buer, "Methods and Apparatus for Implementing a Cryptography Engine", U.S. Patent Application Serial No. 09/892,240, filed June 26, 2001, 35 pages.	
	NPL2	Zheng Qi and Mark Buer, "Methods and Apparatus for Implementing a Cryptography Engine", U.S. Patent Application Serial No. 09/892,310, filed June 26, 2001, 39 pages.	
	NPL3	Zheng Qi and Mark Buer, "Methods and Apparatus for Implementing a Cryptography Engine", U.S. Patent Application Serial No. 09/892,242, filed June 26, 2001, 35 pages.	
	NPL4	Hamer et al. "DES Cracking on the Transmogrifier 2a", CHE'99, LNCS 1717, Springer-Verlag Berlin Heidelberg, 1999, pages 13-24.	
	NPL5	FIPS PUB 46-3 "Data Encryption Standard (DES)", October 25, 1999.	
	NPL6	Microsoft Computer Dictionary Fifth Edition, 2002 Microsoft Corporation, page 427.	
	NPL7	Cetin Kaya Koc, "RSA Hardware Implementation", RSA Laboratories, RSA Data Security, Inc., Redwood City, California, Version 1, August 1995, 28 pages.	
	NPL8	C. Madson and N. Doraswamy, "The ESP DES-CBC Cipher Algorithm With Explicit IV", November 1998, RFC 2405, 8 pages.	
	NPL9	Bruce Schneier, "Applied Cryptography, Protocols, Algorithms, and Source Code in C," Chapter 12: Data Encryption Standard (DES), 1996.	
	NPL10	European Search Report for European Patent Application No. 01308081, dated 13 May 2003.	

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NON PATENT LITERATURE DOCUMENTS			
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	NPL11	Young, Won Lim. "Efficient 8-cycle DES Implementation". ASIC 2000. Pub. August 2000. Pages 175-178. Found on the World Wide Web at: http://ieeexplore.ieee.org/iel5/7202/19406/00896937.pdf?tp=&isnumber=19406&arnumber=896937&punumber=7202 .	
	NPL12	Bonnenberg, H. Curiger, A. Felber, N. Kaeslin, H., Lai, X. "VLSI Implementation of a New Block Cipher". ICCD '91 Proceedings. Pub. October 1991. Pages 510-513. Found on the World Wide Web at: http://ieeexplore.ieee.org/iel2/367/3775/00139960.pdf?tp=&isnumber=3775&arnumber=139960&punumber=367 .	
	NPL13		
	NPL14		
	NPL15		
	NPL16		
	NPL17		
	NPL18		
	NPL19		
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